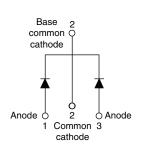


## VS-STPS40L15CTPbF, VS-STPS40L15CT-N3

Vishay Semiconductors

# Schottky Rectifier, 2 x 20 A

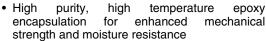




PRODUCT SUMMARY					
Package	TO-220AB				
I <sub>F(AV)</sub>	2 x 20 A				
$V_{R}$	15 V				
V <sub>F</sub> at I <sub>F</sub>	See Electrical table				
I <sub>RM</sub> max.	600 mA at 100 °C				
T <sub>J</sub> max.	125 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	10 mJ				

#### **FEATURES**

- 125 °C T<sub>J</sub> operation (V<sub>R</sub> < 5 V)</li>
- Optimized for OR-ing applications
- · Ultra low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability





- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



## **DESCRIPTION**

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	40	Α			
$V_{RRM}$		15	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	700	Α			
V <sub>F</sub>	19 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg, typical)	0.25	V			
$T_J$		- 55 to 125	°C			

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-STPS40L15CTPbF	VS-STPS40L15CT-N3	UNITS			
Maximum DC reverse voltage	$V_{R}$	15	15	V			
Maximum working peak reverse voltage	$V_{RWM}$	13	13	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDI	TIONS	VALUES	UNITS	
Maximum average	per leg		50 % duty cycle at T <sub>C</sub> = 85 °C, rectangular waveform		20		
forward current See fig. 5	per device	I <sub>F(AV)</sub>			40		
Maximum peak one cycle non-r surge current per leg	epetitive	I	5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated V <sub>RRM</sub> applied		700	Α	
See fig. 7		IFSM			330		
Repetitive avalanche current pe	er leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2		
Non-repetitive avalanche energ	y per leg	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 2  A,  L = 6  \text{mH}$		10	mJ	



# VS-STPS40L15CTPbF, VS-STPS40L15CT-N3

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS			MAX.	UNITS
		19 A	T <sub>.1</sub> = 25 °C	-	0.41	V
Forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	1J=25 C	-	0.52	
See fig. 1	V <sub>FM</sub> ('')	19 A	T 405.00	0.25	0.33	
		40 A	T <sub>J</sub> = 125 °C	0.37	0.50	
Reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V Dated V	-	10	mA
See fig. 2	'RM '''	T <sub>J</sub> = 100 °C	V <sub>R</sub> = Rated V <sub>R</sub>	-	600	TIIA
Threshold voltage	V <sub>F(TO)</sub>	T - T movimum		0.1	82	V
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum	7	.6	mΩ	
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal rang	-	2000	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	8	-	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10	000	V/µs	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBO	DL TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature	range T <sub>J</sub>		- 55 to 125	°C	
Maximum storage temperature	range T <sub>Stg</sub>		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		DC operation See fig. 4	1.5		
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased (only for TO-220)	0.50	°C/W	
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation (for D <sup>2</sup> PAK and TO-262)	40		
Annyayimata waight			2	g	
Approximate weight			0.07	OZ.	
	inimum	Non-lubricated threads	6 (5)	kgf · cm	
Mounting torque ma	aximum		12 (10)	(lbf $\cdot$ in)	
Marking device		Case style TO-220AB	STPS40L15CT		

## Vishay Semiconductors

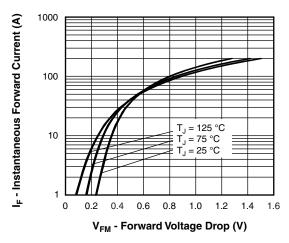


Fig. 1 - Maximum Forward Voltage Drop Characteristics

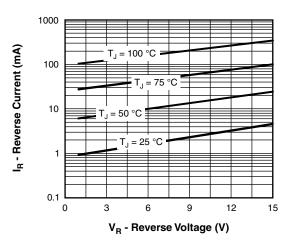


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

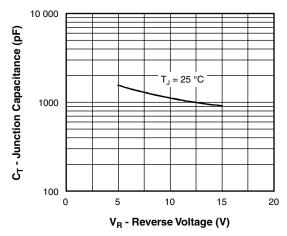


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

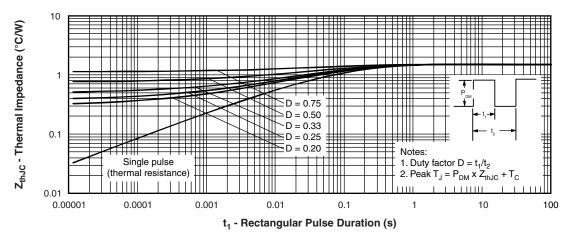


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics

## Vishay Semiconductors

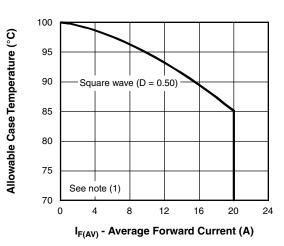


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

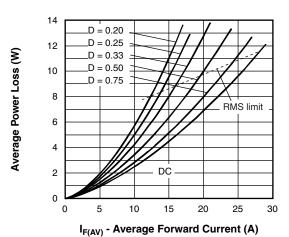


Fig. 6 - Forward Power Loss Characteristics

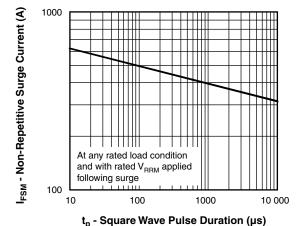


Fig. 7 - Maximum Non-Repetitive Surge Current

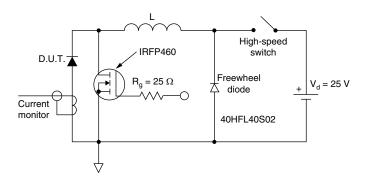


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

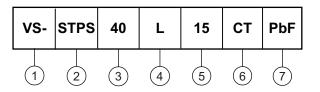
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 

# VS-STPS40L15CTPbF, VS-STPS40L15CT-N3

Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**





1 - Vishay Semiconductors product

2 - Schottky STPS series

3 - Current rating (40 = 40 A)

4 - L = Low voltage drop

5 - Voltage rating (15 = 15 V)

6 - CT = Essential part number

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-STPS40L15CTPbF	50	1000	Antistatic plastic tube			
VS-STPS40L15CT-N3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95222</u>					
Dort marking information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			



## Vishay Semiconductors

## **TO-220AB**

#### **DIMENSIONS** in millimeters and inches



## Lead assignments

### **Diodes**

- 1. Anode/open
- 2. Cathode
- 3. Anode

#### Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	
		•	•	•	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip



## **Legal Disclaimer Notice**

Vishay

## **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.